all Tic	ke	et N	umb	er:								
											Code No. : 21512	
											NG (Autonomous), HYDERABAD cklog) Examinations, Nov./Dec2016	
								Mic	ero E	Clec	tronics	
Γime:	3	hou		ta. As	a muli	on A1	I am	actio	ne in	Davi	Max. Marks: 70 -A and any FIVE from Part-B	
			1101	e. Ar	IS W	AL					= 20 Marks)	
l. Dr	-91	w th	e cir	cuit (svm	hols					varactor diode.	
							hich r					
											te BJT as a switch?	
				, ,	-						er, Base and Collector regions.	
		-		-			of JFI					
				OS lo								
7. W	ri	te th	ne ex	press	sion	for c	losed	loop	gain	of a	positive feedback Amplifier.	
B. Di	ra	w th	ie sc	hema	tic s	symb	ol of	a cry	stal o	scill	ator.	
). Di	ra	w th	ie cir	cuit :	for u	ısing	op-A	mp a	as a d	iffer	entiator.	
0. W	h	at is	the	gain	of o	p-am	p in t	he in	verti	ng co	onfiguration?	
							Pa	ırt-B	(5 ×	10 =	= 50 Marks)	
1. a)		Exp	lain	abou	t bia	ising	meth	ods o	of a se	emic	conductor diode.	[4]
b))	Wit	har	neat c	ircu	it dia	agram	, des	cribe	the f	functioning of Bridge-wave rectifier.	[6]
2. a)		Wit	har	eat c	ircu	it dia	gram,	exp	lain h	ow t	o use bipolar junction transistor as an amplifier.	[5]
b))	Dra	w th	e sma	all s	ignal	h-pai	rame	ter eq	uiva	llent model of bipolar junction transistor.	[5]
3. a))	Hov	w a N	MOSI	FET	can	be us	ed as	a sw	itch'	?	[5]
											logic.	[5]
4. a)		Dis	cuss	the a	dva	ntago	es of r	negat	ive fe	edh	ack	[6]
											negative feedback amplifier.	[4]
					•							
,		^			•		•			_	above circuit.	[5]
												[5]
l 6. a)			th ne cuits.		rcuit	diag	grams	, exp	lain t	he f	unctioning of positive and negative clamping	[6]
b))	Der	ive t	he re	latio	onshi	ip bety	ween	curre	ent a	mplification factors ' α ' and ' β '.	[4]
							llowi					
				l stru se shi			MOS!	FET				[5]
c)			-				nor plifier	usir	g Op	-Am	np.	[5] [5]
